

US2AA THRU US2MA

VOLTAGE RANGE

2.0 AMP SURFACE MOUNT ULTRA FAST RECTIFIER



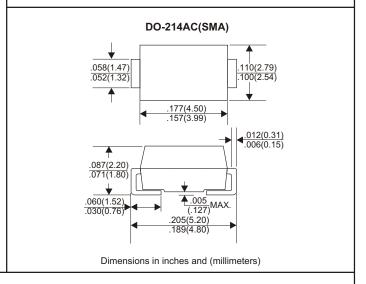
FEATURES

- * Ideal for surface mount applications
- * Easy pick and place
- * Built-in strain relief
- * Fast switching speed

MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-0 rate flame retardant
- * Metallurgically bonded construction
- * Polarity: Color band denotes cathode end
- * Mounting position: Any * Weight: 0.063 grams

50 to 1000 Volts CURRENT 1.0 Ampere



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwies specified. Single phase half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

TYPE NUMBER	US2A	US2B	US2D	US2E	US2G	US2J	US2K	US2M	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	300	400	600	800	1000	V
Maximum RMS Voltage	35	70	140	210	280	420	560	700	V
Maximum DC Blocking Voltage	50	100	200	300	400	600	800	1000	V
Maximum Average Forward Rectified Current									
.375"(9.5mm) Lead Length at Ta=100°C		2.0							
Peak Forward Surge Current, 8.3 ms single half sine-wave									
superimposed on rated load (JEDEC method)		50						Α	
Maximum Instantaneous Forward Voltage at 2.0A		1.0			.3		1.7		V
Maximum DC Reverse Current Ta=25°C		5.0						μА	
at Rated DC Blocking Voltage Ta=100°C		150							μА
Maximum Reverse Recovery Time (Note 1)		50 75				nS			
Typical Junction Capacitance (Note 2)		30							рF
Operating and Storage Temperature Range Тл, Тsтс		-55—+150							°C

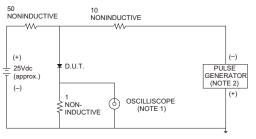
NOTES

- 1. Reverse Recovery Time test condition: IF=0.5A, IR=1.0A, IRR=0.25A
- 2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

RATING AND CHARACTERISTIC CURVES (US2AA THRU US2MA)

FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS

FORWARD VOLTAGE,(V)



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF.

2. Rise Time= 10ns max., Source Impedance= 50 ohms.

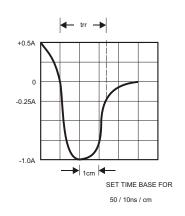


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

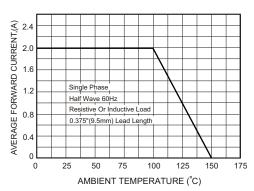


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

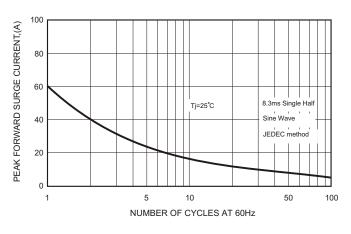
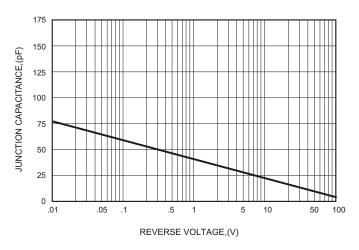


FIG.5-TYPICAL JUNCTION CAPACITANCE



Attention:

The graph is for reference only, can't be the basis for judgment.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement.